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SUMMARY

Experienced engineer and leader specializing in quickly identifying and solving complex engineering and manufacturing issues. Proven track record of reducing costs and lead time while improving quality and performance in a wide array of products and processes. Utilizes test and measurement tools and innovative statistical analysis to identify root cause and develop models to predict necessary parameters to obtain desired results. Dedicated team player with excellent interpersonal skills. Seeking permanent or contract work in engineering, manufacturing and/or leadership roles solving significant technical problems.

EXPERIENCE

1992 - 2009 TYCO ELECTRONICS
(Precision Interconnect subsidiary)
Wilsonville, OR *Principal Engineer*

- Developed measurement systems to measure product and process characteristics. These include measurements of high voltage to 10kV, low resistance to 10mOhms, high resistance to 1TOhm, characteristic impedance repeatability to 0.05Ohms, rise time to 0.1ps, skew to 0.1ps, S-parameters from 30kHz to 40GHz.
- Implemented new measurement instrument system in 1 month, new product test in 2 hours. Built the design tools and models for coax cables.
- Developed SPICE models for coax and pair transmission lines.
- Worked with customers, designers and manufacturing people to identify the root causes of unusual major technical problems in few days. The impact for local experts not resolving the problems would have been significant loss of business. These problems occurred between 3 to 7 times per year, both in the U.S. and México.
- Coached peers in the usage of statistical tools and problem-solving techniques. Led the design of over 20 experiments.
- Advice sought weekly by Managers and other Principal Engineers.

1990 - 1992 ZEELAN TECHNOLOGY
Beaverton, OR *Engineering Manager*

- Developed hardware and measurement algorithms for a CAE station that extracts DC and high frequency mathematical models of physical components.
- Designed and developed the op amp model and brought it into production status in 9 months. Designed the behavioral model and demonstrated prototypes of a transistor model and high power device model.
- Contributed to the development of patentable pole-zero extraction software algorithms.
- Supervised the design of 21 circuit boards and the work of 3 engineers and 2 technicians.
- Awarded stock options on three different occasions as the result of my contributions to the company.

1985 - 1990 TEKTRONIX
Beaverton, OR
Measurement & Accessories Division *Engineering Manager*

- Developed the strategic framework for new product development. During the next 4 years, the number of new products introduced each year grew from 5 to 20. The division was among the three most profitable within the company. This was accomplished with a staff where 63% of the engineers had 3 years of experience or less. Over 4 years, the number of patents applied for by the division grew from 0 to an average of 10 per year. Engineering expenses were always within 2% of budget.
- My contributions were recognized with the granting of stock options in four successive years.
- Developed a methodology and tools that improved quality and identified the causes of waste in any manufacturing process.

1976 - 1985 TEKTRONIX
Beaverton, OR
Portable Instruments Division *Section Manager*

- Identified a new business opportunity which required the change to a new digital technology. In contrast, another division lost its leadership position because they continued using the old technology.
- The first product my group developed using the new technology achieved the highest sales volume in its class in the world. Within one year of its introduction, it became one of the top 10 profit generators for Tektronix. This product was developed in 18 months, while the rest of the division had not introduced a new product in 5 years. The development costs were 5% of one year's sales. The follow-up products I developed and their successors have all remained within the top 10 profit generators for Tektronix. My group expenses were always within 2% of budget.
- Organized a professional Manufacturing Engineering group at a new site. During the first year we saved $5M with a budget of $500K.
- During this time, I was promoted from Project Leader to Section Manager. My contributions were recognized with the granting of stock options over several years. My innovations have resulted in 6 U.S. patents, two of which were successfully litigated.
- As an expert, I was invited and presented a paper in Japan. I was also asked to write an Anniversary Edition article for a major trade journal circulated throughout all Spanish-speaking countries.

1966 - 1976 TEKTRONIX
Beaverton, OR
Advanced Product Development *Project Leader*

- Developed Tektronix's first Digital Processing Oscilloscope and interfaced it to a PDP11.
- Developed the world's first 500MHz counter integrated circuit.
- Evaluated Tektronix's first portable Physiological Monitor (Model 410).
- One of the products I developed was manufactured at 70% of the cost of brand new line but featured a 67% increase in performance. This product was removed from the catalog after 17 profitable years. In contrast, our main competitor abandoned this market segment within 3 years after the introduction of my product.

EDUCATION

UNIVERSITY OF NEBRASKA
Lincoln, NE

- M.S.E.E., B.S.E.E. Inducted into Sigma Tau and Eta Kappa Nu, Engineering Honorary Societies.

TEKTRONIX, INC.

- Manager of Managers Program: Two year, eight-module program developed by Tektronix to sharpen the skills of its most promising managers.
- The Marketing Management Program: One-week seminar by Columbia University Graduate School of Business.
- Statistical Analysis: Various seminars on statistical tools for problem solving.
- Seminars in Project Management, Selection for Excellence, Organizational Development, Coaching and Negotiating.

HONORS

- Invited to write an article for anniversary issue of *Mundo Electrónico*, a major trade magazine in Spain.
- Inducted to Electronic Design magazine Engineering Hall of Fame for the development of digital oscilloscopes (http://www.elecdesign.com/Articles/Index.cfm?ArticleID=5841).
- Published papers:
  - *Measurement of Characteristic Impedance of High Frequency Cables* presented at ARFTG 2008.
  - *Application of Launch Point Extrapolation Technique to Measure Characteristic Impedance of High Frequency Cables with TDR* presented at DesignCon 2009

INTERESTS

Traveling, soccer, dancing, cultural differences, metal sculpting and spending time with our family.